

SEP 22 2004

TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
FIS920030191 (00750480AA)

In Re Application of Chan et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/695,754	10/30/2003	F. Erdem	30743	2826	6186

Title: **INCREASING CARRIER MOBILITY IN NFET AND PFET TRANSISTORS ON A COMMON WAFER**

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

**Response To Requirement for Restriction
Postcard**

in the above identified application.

- ☒ No additional fee is required.
- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **09-0458**
as described below.
- ☐ Charge the amount of _____
- ☒ Credit any overpayment.
- ☒ Charge any additional fee required.
- ☐ Payment by credit card. Form PTO-2038 is attached.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.


Signature

Dated: September 22, 2004

Marshall M. Curtis
Reg. No. 33,138

Whitham, Curtis & Christofferson, P.C.
11491 Sunset Hills Road, Suite 340
Reston, VA 20190
(703) 787-9400

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] o.
(Date)

Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence

CC:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Victor Chan et al.

Conf. No.: 6186

Serial No.: 10/695,754

Group Art Unit: 2826

Filed: October 30, 2003

Examiner: F. Erdem

For: INCREASING CARRIER MOBILITY IN NFET AND PFET
TRANSISTORS ON A COMMON WAFER

Commissioner for Patents
United States Patent and Trademark Office
P. O. Box 1450
Alexandria, Virginia 22313-1450

RESPONSE TO REQUIREMENT FOR RESTRICTION

Sir:

In response to the Office Action mailed August 25, 2004, Applicants provisionally elect, with traverse, the invention identified by the Examiner of Group II, claims 1 - 11.

REMARKS

Claims 1 - 20 remain active in this application. No new matter has been introduced into the application.

Restriction has been required between the inventions, as identified by the Examiner of Group I, claims 12 - 20, drawn to a semiconductor device, and Group II, claims 1 - 11, drawn to a method of making a semiconductor device. Election of the invention of Group II, claims 1 - 11 for further prosecution on their merits has been made with traverse above.

The requirement for restriction is respectfully traversed because the Examiner has not, in fact, made a